

LT6911B --- Product Brief

HDMI1.4 to Quad-port MIPI DSI/CSI with Audio

Features

HDMI1.4 Receiver

- Compliant with the HDMI 1.4 specification with TMDS data rates up to 3.4Gbps per channel
- Support HDCP 1.4
- Adaptive receiver Equalization for PCB, cable and connector losses

Single/Dual-Port/Quad-Port MIPI® DSI/CSI

Transmitter

- Compliant with DCS1.02, D-PHY1.2& DSI1.02 & CSI-2 1.0
- 1 Clock Lane, and 1~4 Configurable Data Lanes per port
- 1/2/4 configurable port
- 80Mb/s~1.5Gb/s per data lane
- Data lane and polarity swapping
- Maximum 64pixels overlap for each half
- Both non-burst and burst video mode supported
- Support RGB666, Loosely RGB666, RGB888, RGB565, 16-bit YCbCr4:2:2, 20-bit YCbCr4:2:2, 24-bit YCbCr 4:2:2, 12-bit YCbCr4:2:0 Video Format
- Video stream copy mode for each single/dual-port
- Side-by-side 3D support
- Port swap

Miscellaneous

- 3.3V/1.2V Supply Power
- Internal CSC support conversions between YCbCr 4:4:4 and RGB, and between YCbCr 4:2:2 and YCbCr 4:4:4
- Support SPDIF and 8-channel IIS audio output
- Support 100KHz and 400KHz I2C slave
- Power from phone or adapter mode selection
- Integrated Microprocessor

Applications

Mobile system

Display

VR

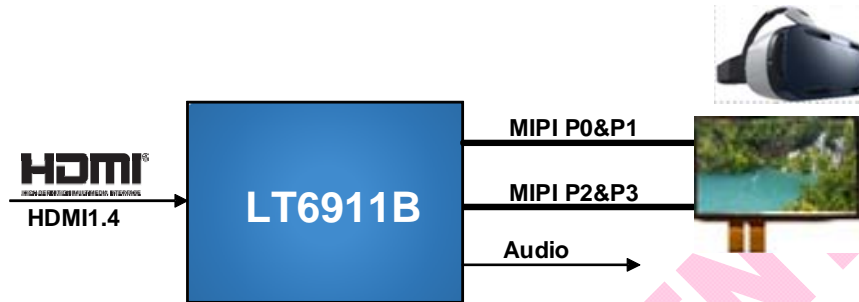


Figure1. Application Diagram

Ordering Information

Part Number	Operating Temperature Range	Package	Packing Method
LT6911B	-40 to+85	BGA144 (7*7)	Tray

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