

- f* Support Center Spread Spectrum , 30kHz/5000ppm
- z Dual-Port MIPI DSI/CSI Transmitter
 - f* Compliant with DCS1.1, D-PHY1.2, DSI1.3 and CSI-2 1.3
 - f* 1/2 configurable port number
 - f* 1 clock lane and 1/2/3/4 configurable data lanes per port
 - f* 80Mbps~2Gbps per data lane
 - f* Support data lane swap(arbitrarily) and polarity inversion(independent)
 - f* Programmable transmitter swing and pre-emphasis
 - f* Support Non-Busrt mode, Burst mode and Command mode
 - f* DSI support video formats: RGB565/666/888, Loosely RGB666, 16/24-bit YCbCr4:2:2, Loosely 20-bit

- YCbCr4:2:2, 12-bit YCbCr4:2:0
- f* CSI support video formats: RGB565/666/888, YUV422 8/10-bit, Legacy YUV420 8-bit
- f* Support de-skew function
- z Miscellaneous
 - f* Internal or external oscillator
 - f* Integrated microprocessor
 - f* Embedded SPI flash for firmware
 - f* Integrated 100/400kHz I2C slave
 - f* Firmware update through SPI or I2C interface
 - f* UART Support
 - f* Backlight Control Support
 - f* Low power consumption
 - f* Power supply: 3.3V for I/O and 1.2V for core
 - f* Temperature Range: -40°C ~ +85°C
 - f* 76 pin QFN 9*9 package

Description

LONTI

Copyright © 2019 Lontium Semiconductor Corporation, All rights reserved.

Lontium Semiconductor Proprietary & Confidential

This document and the information it contains belong to Lontium Semiconductor. Any review, use, dissemination, distribution or copying of this document or its information outside the scope of a signed agreement with Lontium is strictly prohibited.

LONTIUM DISCLAIMS ALL WARRANTIES, EXPRESSED OR IMPLIED, INCLUDING THOSE OF NONINFRINGEMENT, MERCHANTABILITY, TITLE AND FITNESS FOR A PARTICULAR PURPOSE. CUSTOMERS EXPRESSLY ASSUME THEIR OWN RISK IN RELYING ON THIS DOCUMENT.

LONTIUM PRODUCTS ARE NOT DESIGNED OR INTENDED FOR USE IN LIFE SUPPORT APPLIANCES, DEVICES OR SYSTEMS WHERE A MALFUNCTION OF A LONTIUM DEVICE COULD RESULT IN A PERSONAL INJURY OR LOSS OF LIFE.

Lontium assumes no responsibility for any errors in this document, and makes no commitment to update the information contained herein. Lontium reserves the right to change or discontinue this document and the products it describes at any time, without notice. Other than as set forth in a separate, signed, written agreement, Lontium grants the user of this document no right, title or interest in the document, the information it contains or the intellectual property it embodies.

Trademarks

Lontium™ and ClearEdge™ is a registered trademark of Lontium Semiconductor. All other brand names, product names, trademarks, and registered trademarks contained herein are the property of their respective owners.